



G72811
Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Vora

Art Unit: 2811

Examiner: Sara Crane

Serial No. 08/654,760

Filed: 5/29/96

For: VERTICALLY INTEGRATED FLASH EEPROM FOR GREATER DENSITY AND LOWER COST

Honorable Commissioner
of Patents and Trademarks
Washington, D.C. 20231

Morgan Hill, California
June 10, 1998

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SUPPLEMENTAL RESPONSE

GROUP 2100

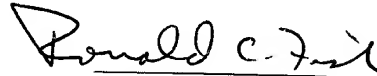
Sir:

In the above-cited application, an amendment in response to the Final Rejection was filed yesterday 6/9/98. That amendment was supposed to have enclosed two markups of Figure 33

Patent

and Figures 3 and 4. Those markups were inadvertently omitted. The markups are enclosed herewith for review by the Examiner.

Respectfully submitted,

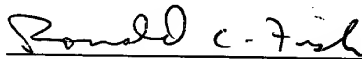


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Dated: June 10, 1998

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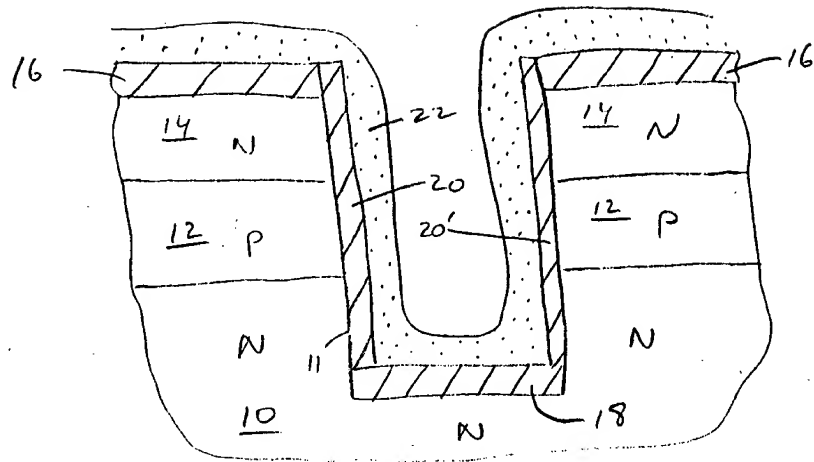


FIG. 1

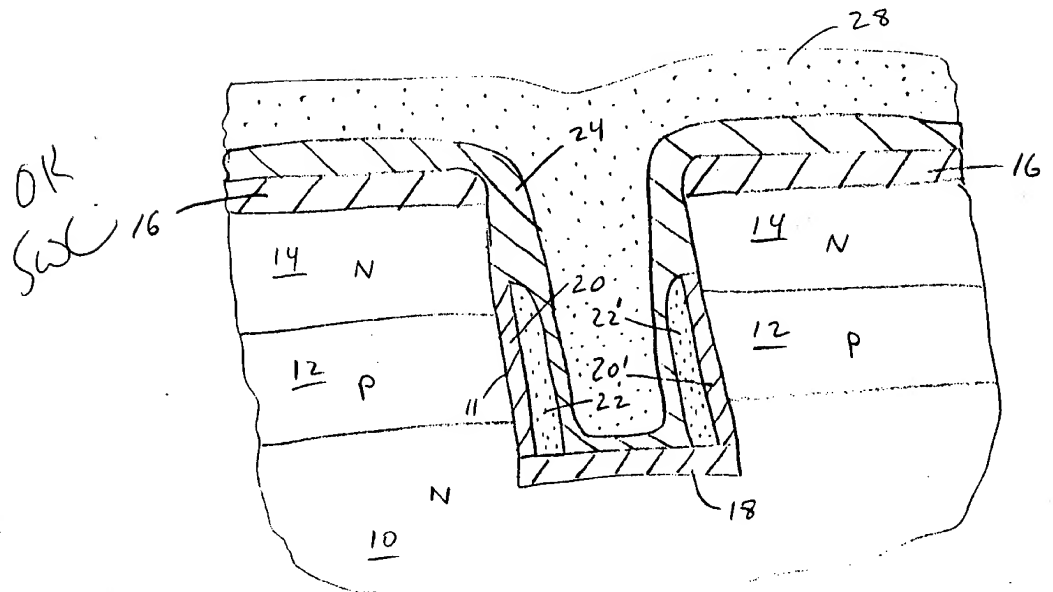
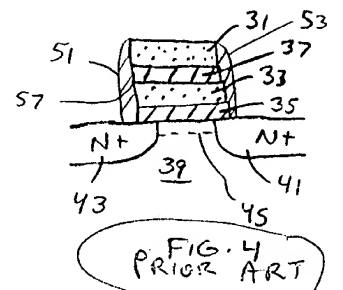
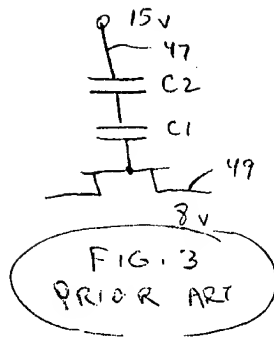


FIG. 2



P70.
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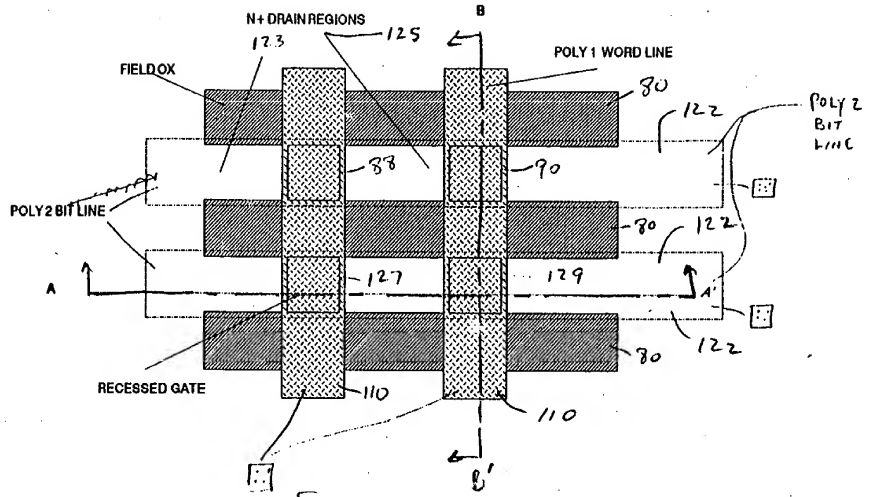


FIG. 32
TOP VIEW - FINISHED CELLS (FOUR)

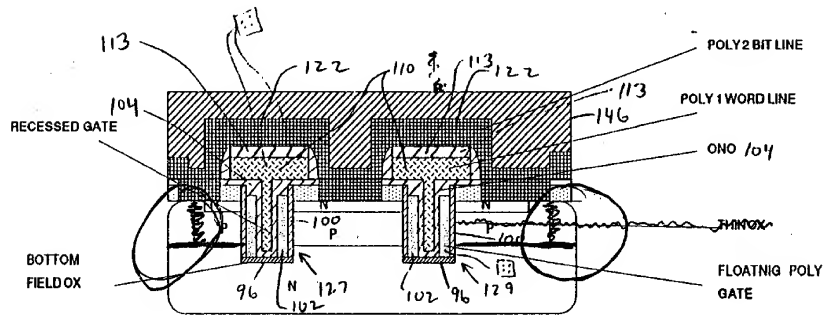


FIG. 33
SECTION A-A'

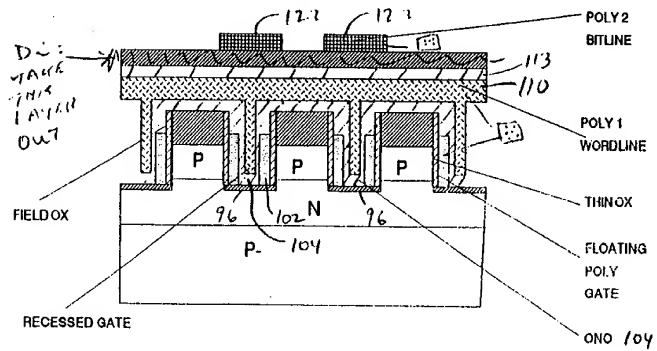


FIG. 34
SECTION B-B'

OK
SWC